

11002 U.S. PTO
09/931125



Class	Subclass	ISSUE CLASSIFICATION

PATENT NUMBER

U.S. UTILITY Patent Application

SCANNED <i>ML</i>	O.I.P.E. <i>IRY</i>	PATENT DATE <i>GW</i>
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APPLICATION NO. 09/931125	CONT/PRIOR	CLASS 714	SUBCLASS 112	ART UNIT 2133	EXAMINER
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APPLICANTS
TITLE

Randall Rooney
Joerg Vollrath

Pseudo fail bit map generation for RAMS during component test and burn-in in a manufacturing environment

PTO-2040
12/99

ISSUING CLASSIFICATION									
ORIGINAL				CROSS REFERENCE(S)					
CLASS		SUBCLASS		CLASS		SUBCLASS (ONE SUBCLASS PER BLOCK)			
INTERNATIONAL CLASSIFICATION									

☐ Continued on Issue Slip Inside File Jacket

<input type="checkbox"/> TERMINAL DISCLAIMER <input type="checkbox"/> The term of this patent subsequent to _____ (date) has been disclaimed. <input type="checkbox"/> The term of this patent shall not extend beyond the expiration date of U.S. Patent. No. _____ <input type="checkbox"/> The terminal _____ months of this patent have been disclaimed.	DRAWINGS Sheets Drwg. Figs. Drwg. Print Fig.			CLAIMS ALLOWED Total Claims Print Claim for O.G.	
	(Assistant Examiner) (Date)			NOTICE OF ALLOWANCE MAILED	
	(Primary Examiner) (Date)			ISSUE FEE Amount Due Date Paid	
	(Legal Instruments Examiner) (Date)			ISSUE BATCH NUMBER	

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